

5675645

1/8

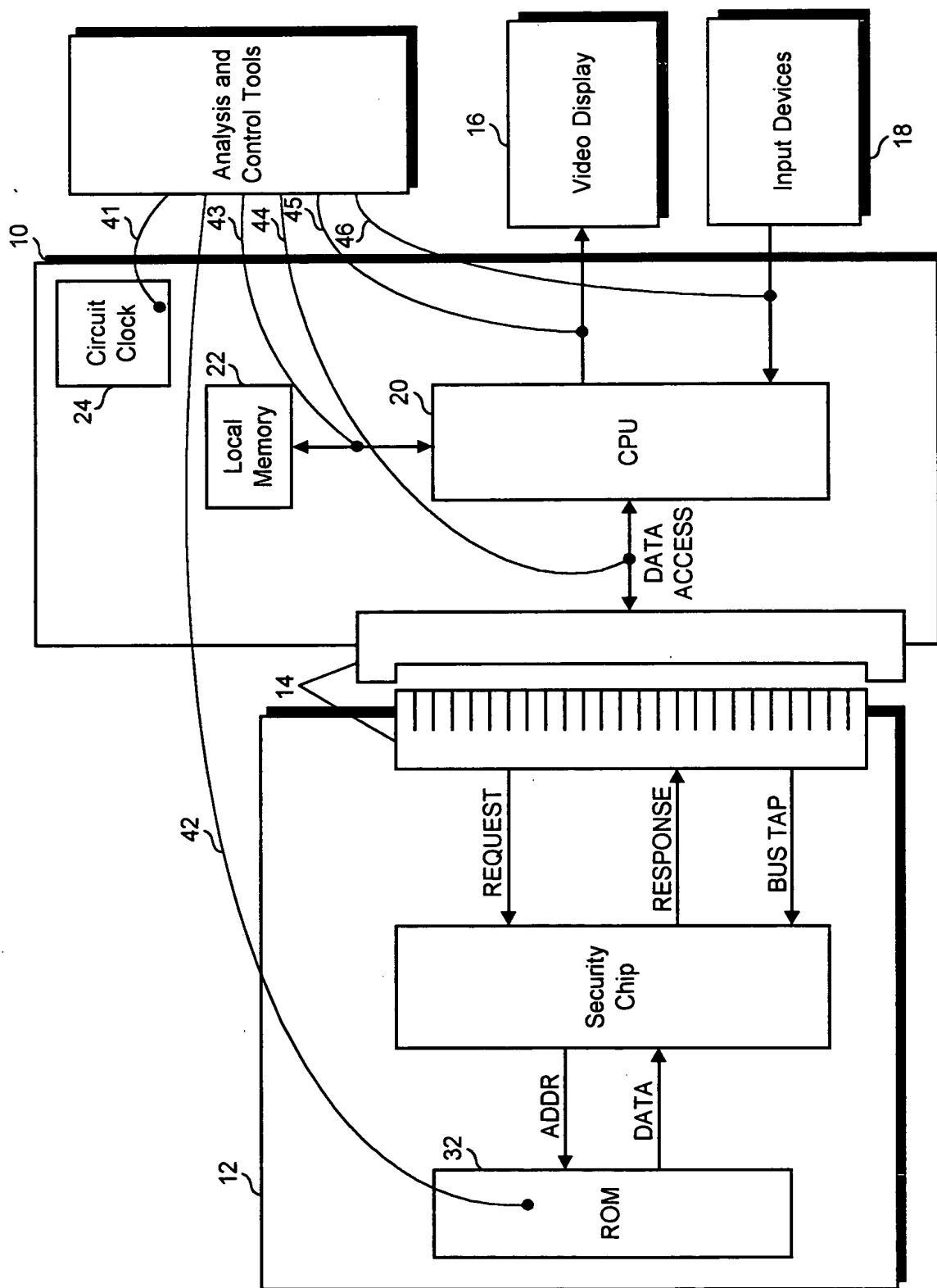


Figure 1

| | | |
|-----------|-------------|----------|
| APPROVED | O.G. FIG. 3 | |
| BY | CLASS | SUBCLASS |
| DRAFTSMAN | 380 | 024 |

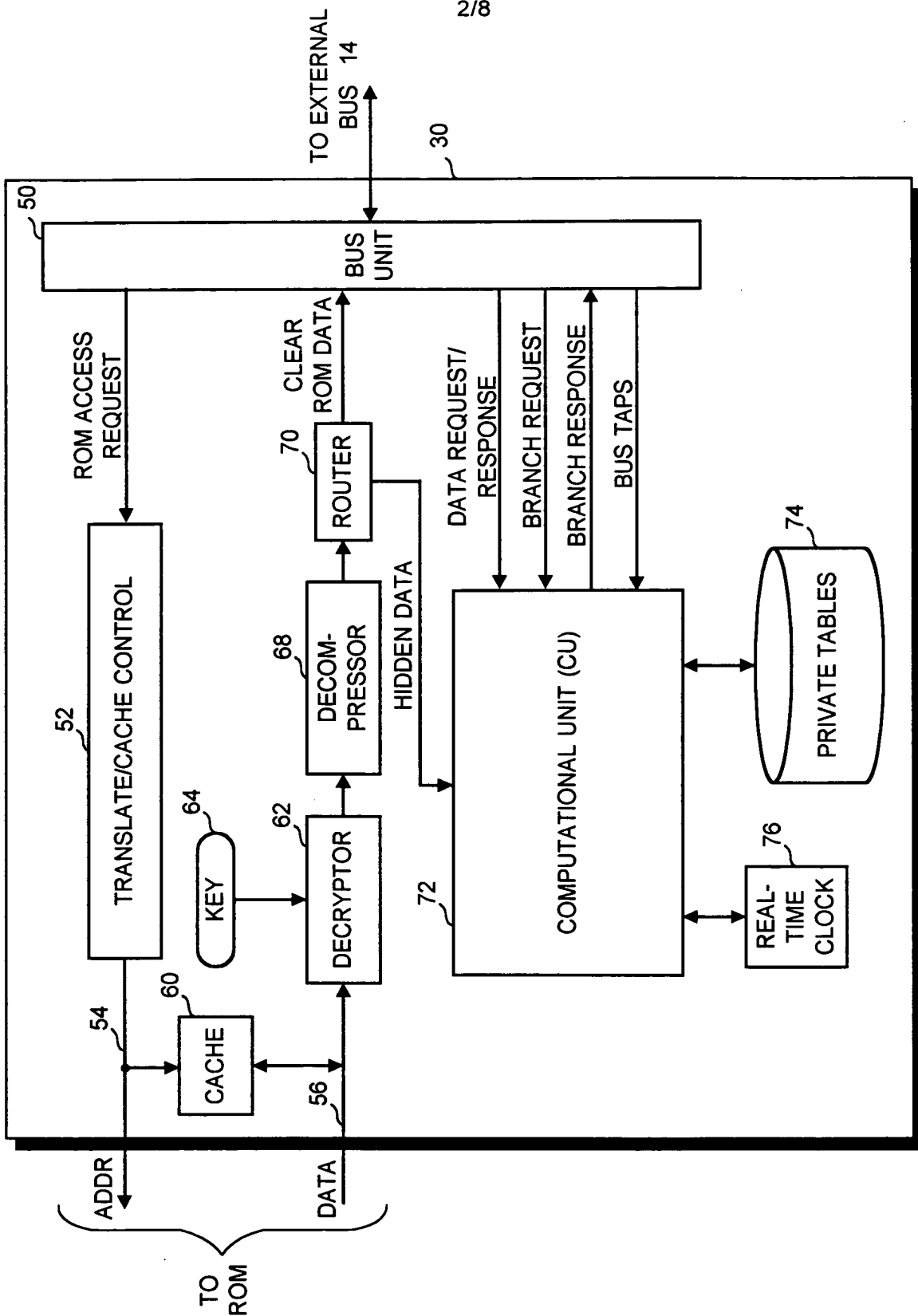


Figure 2

| | |
|-------------|-----------|
| O.G. FIG. 3 | |
| APPROVED | BY |
| CLASS | DRAFTSMAN |
| SUBCLASS | |
| 380 | 274 |

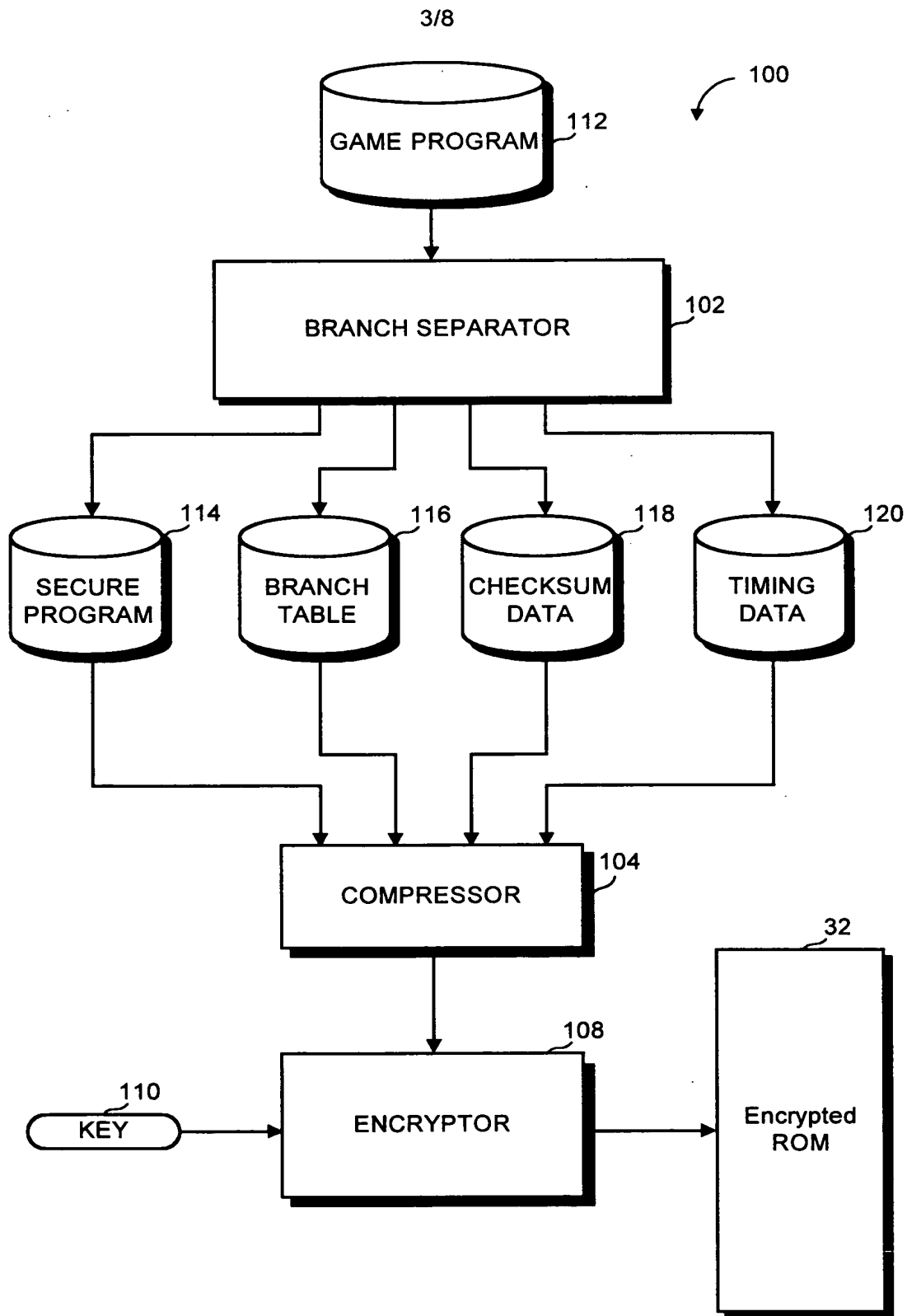


Figure 3

Security Chip

Processor

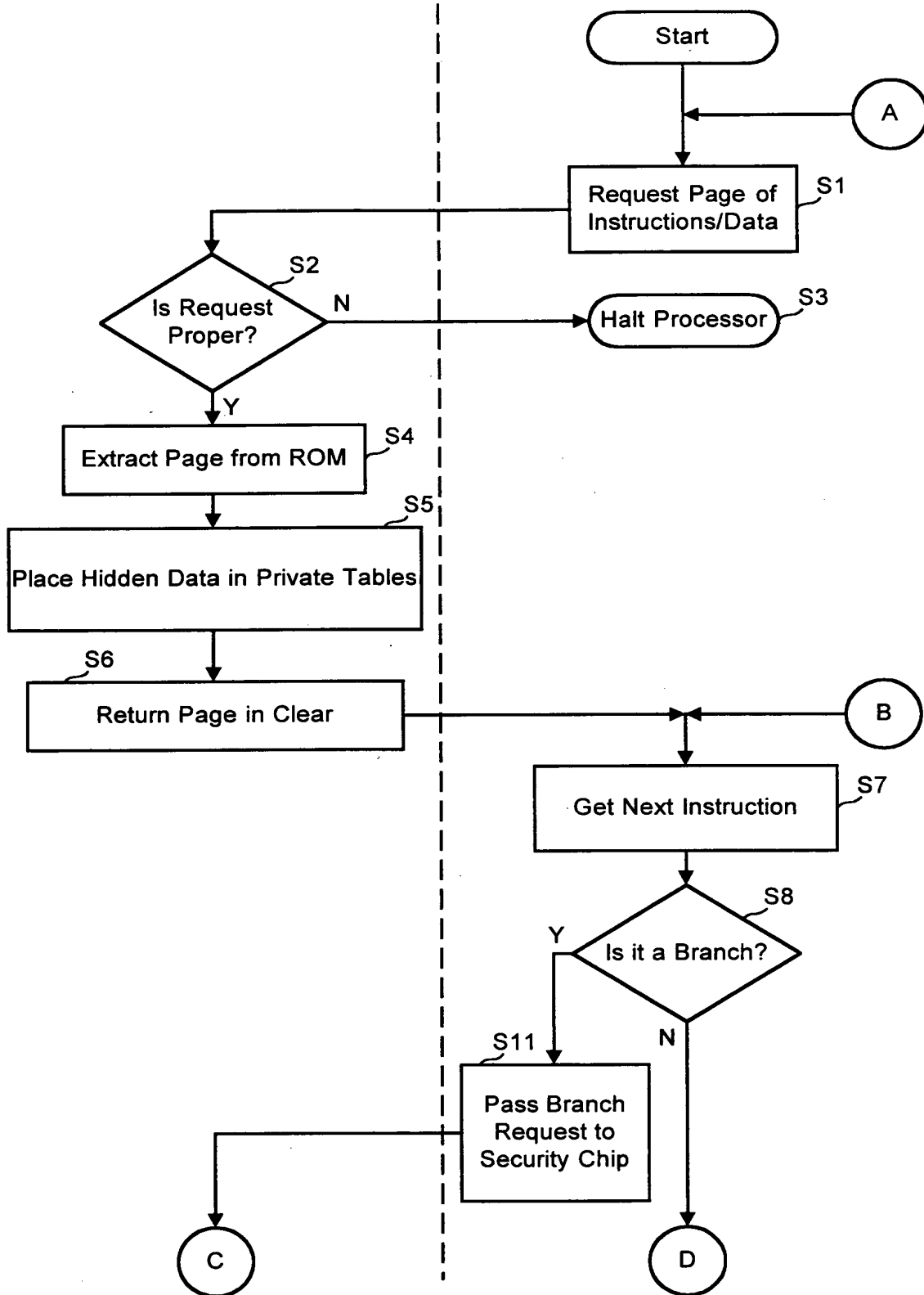


Figure 4A

| | | |
|-----------|-------|-----------|
| O.G. FIG. | CLASS | SUBCLASS |
| APPROVED | BY | DRAFTSMAN |

| | |
|-------------|-----------|
| O.G FIG. | |
| CLASS | SUBCLASS |
| APPROVED BY | DRAFTSMAN |

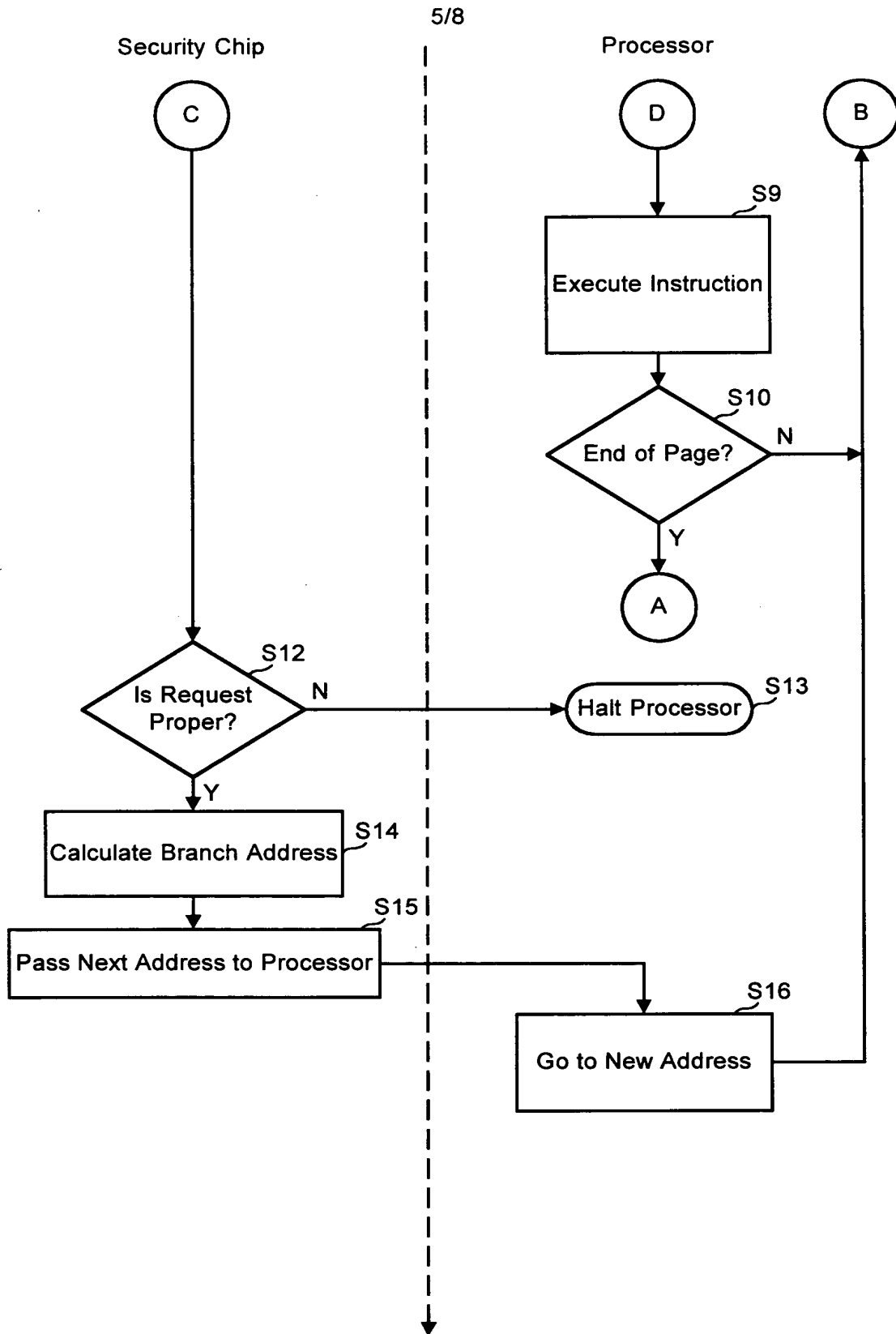


Figure 4B

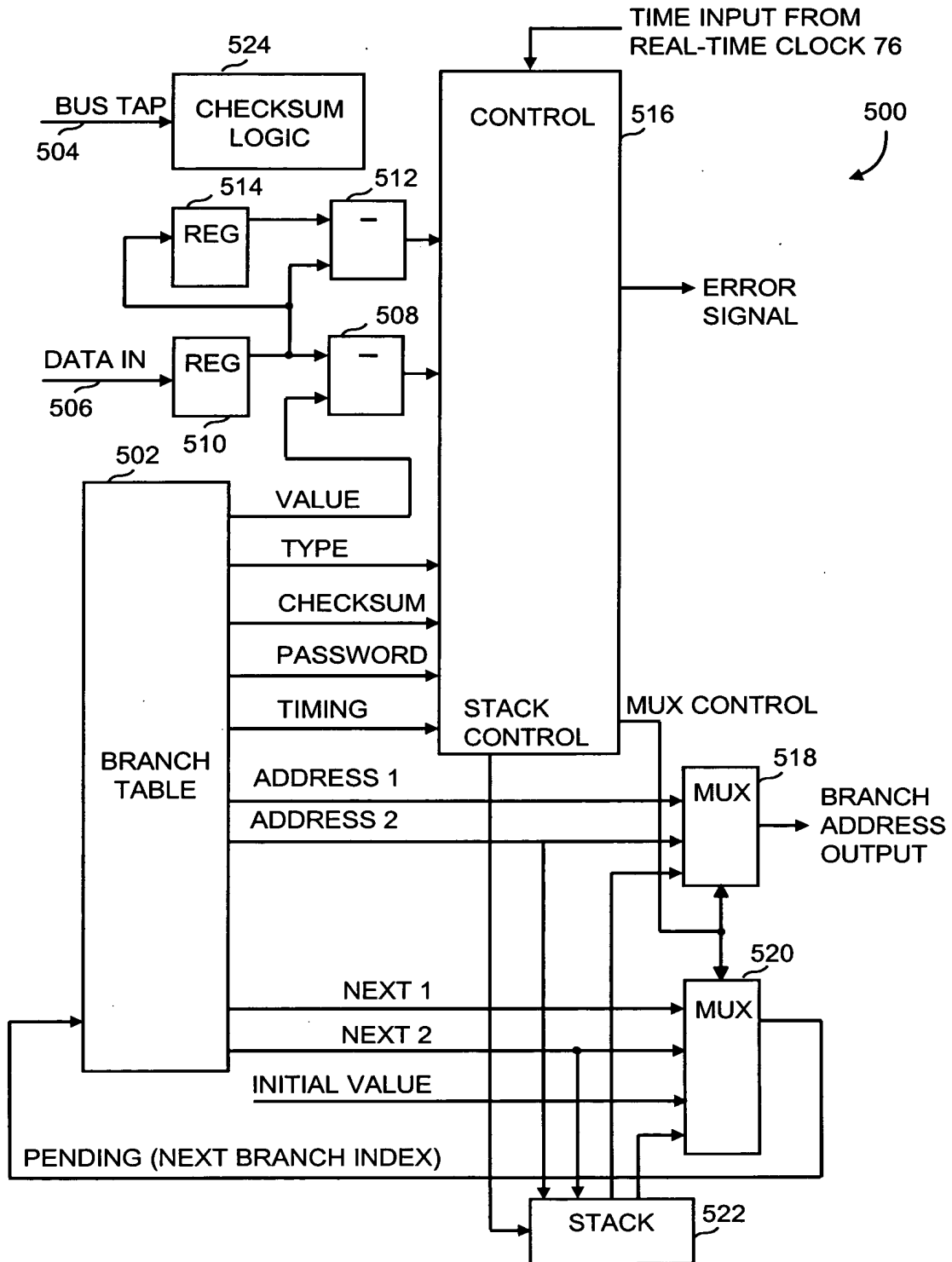


Figure 5

| | | |
|-----------|-----------|----------|
| APPROVED | O.G. FIG. | |
| | CLASS | SUBCLASS |
| BY | | |
| DRAFTSMAN | | |

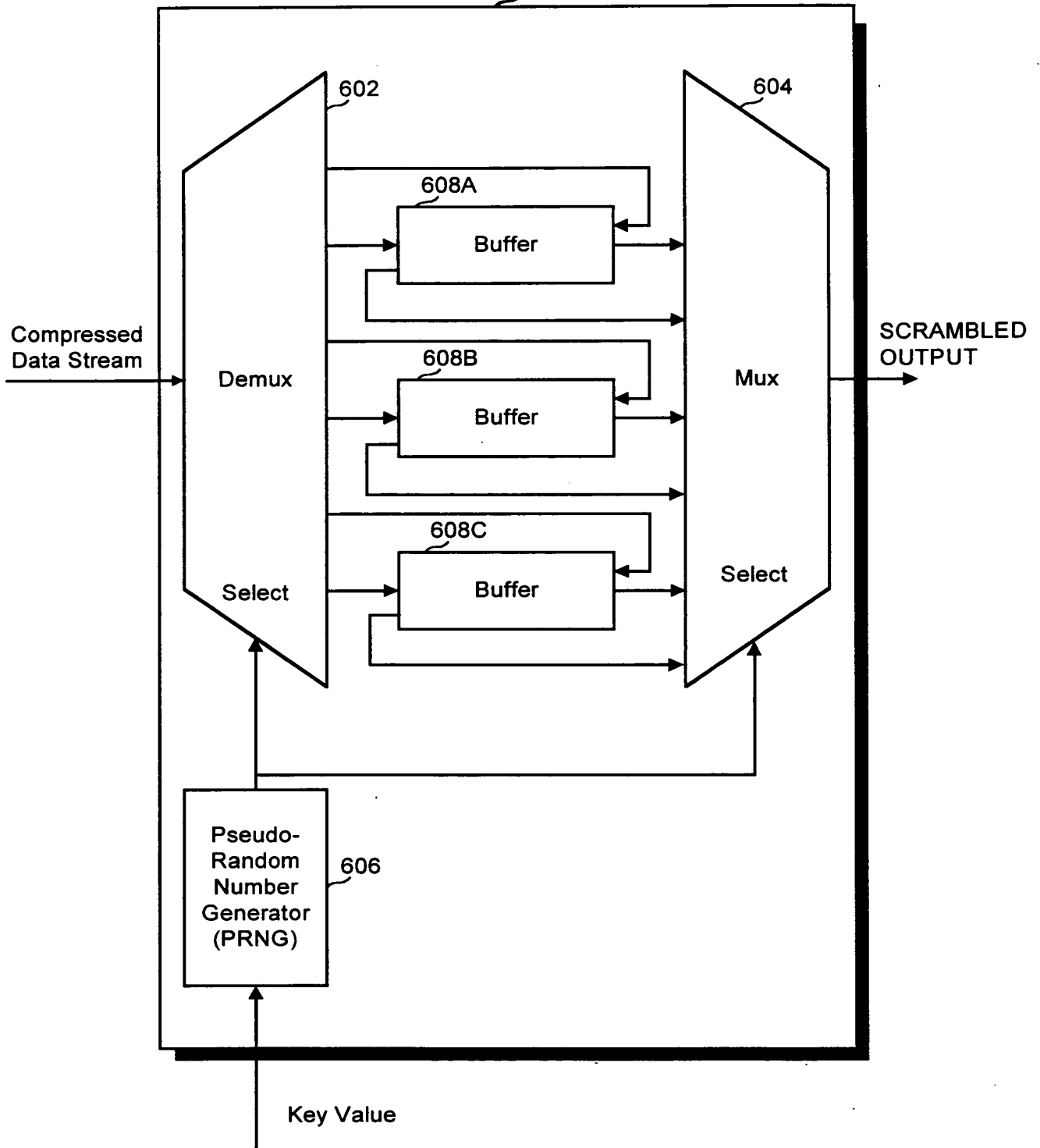


Figure 6

| | | |
|-----------|-----------|----------|
| APPROVED | O.G. FIG. | |
| | CLASS | SUBCLASS |
| BY | | |
| DRAFTSMAN | | |

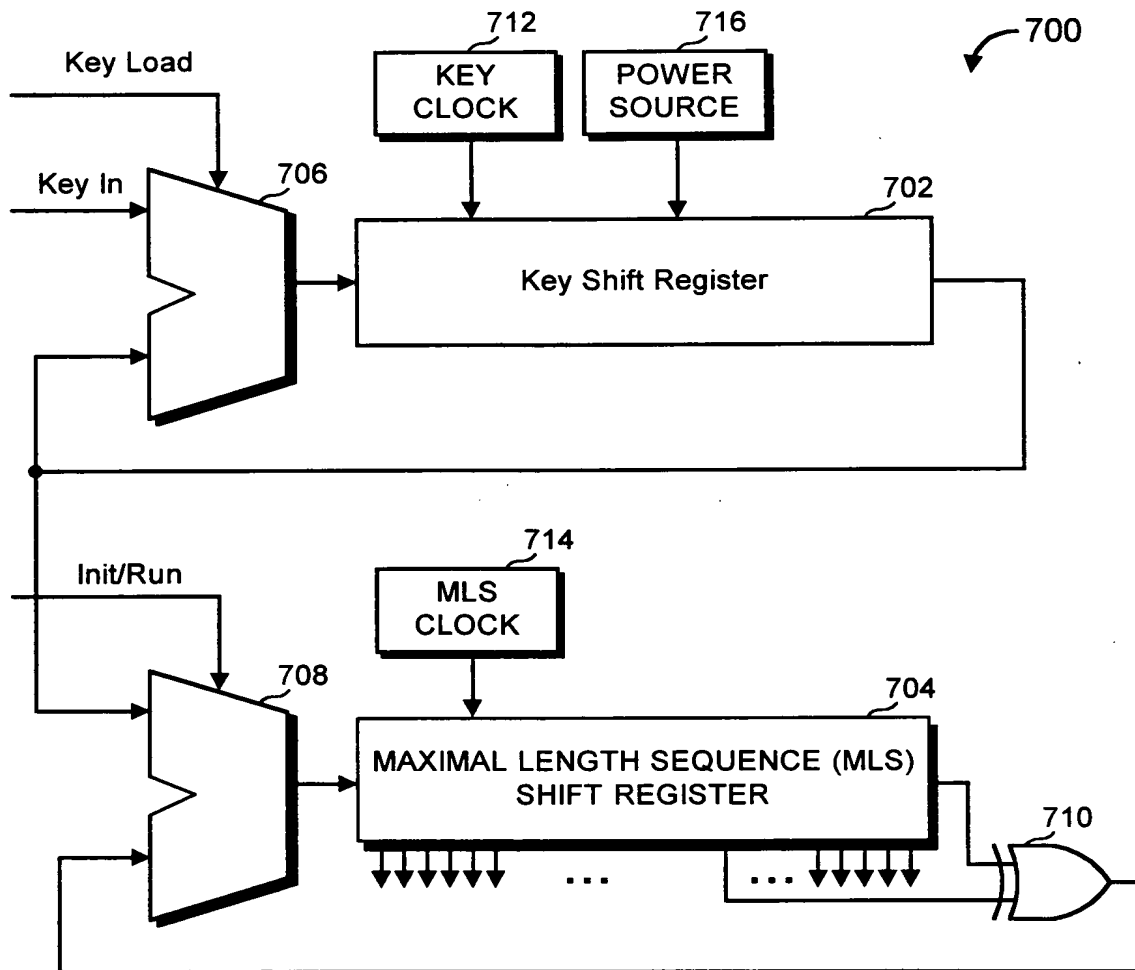


Figure 7